

## PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449		ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		200301885-2		
(Use several sheets if necessary)		APPLICANT		
		Samu I H. Duncan		
		FILING DATE	GROUP	
		July 1, 2003	N/A	

## REFERENCE DESIGNATION                    U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
<i>Mel</i>	1A	5,515,523 ✓	05-07-1996	Kalkunte, et al.	
<i>Mel</i>	1B	5,625,779 ✓	04-29-1997	Solomon, et al.	
<i>Mel</i>	1C	5,884,100 ✓	03-16-1999	Normoyle, et al.	
<i>Mel</i>	1D	6,014,690 ✓	01-11-2000	VanDoren, et al.	
<i>Mel</i>	1E	6,094,686 ✓	07-25-2000	Sharma	
<i>Mel</i>	1F	6,247,102B1 ✓	06-12-2001	Chin, et al.	
	1G				
	1H				
	1I				
	1J				
	1K				

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>Mel</i>	1Q ✓	Chapter 3 Memory Management, <a href="http://www.linuxdoc.org/LDP/tik/mm/memory.html">http://www.linuxdoc.org/LDP/tik/mm/memory.html</a> , (c) 1996-1999, pp. 1-23.
<i>Mel</i>	1R ✓	Mutliprocessors (7 pages).
<i>Mel</i>	1S ✓	RUSSO, Andrew P., The AlpaServer 2100 I/O Subsystem, <a href="http://www.digital.com">http://www.digital.com</a> , (c) 1994, pp. 1-13.

EXAMINER

DATE CONSIDERED

## PATENT APPLICATION

Sheet 2 of 2

FORM PTO-1449		ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
		200301885-2		
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(Use several sheets if necessary)		Samuel H. Duncan		
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2A				
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2L					
2M					
2N					
2O					
2P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

✓	2Q	DUNCAN, S., KEEFER, C. and MCLAUGHLIN, T., High Performance I/O Design in the AlphaServer 4100 Symmetric Multiprocessing System, Compaq Digital Technical Journal, pp. 1-24.
✓	2R	STEINMAN, M., HARRIS, G., KOCEV, A., LAMERE, V. and PANNELL, R., The AlphaServer 4100 Cached Processor Module Architecture and Design, Digital Technical Journal, pp. 1-29.
✓	2S	BANNON, P., Alpha 21364: Scalable Single-chip SMP, (c) 1999, pp. 1-4.

EXAMINER	DATE CONSIDERED
<i>Tim V</i>	<i>5/12/04</i>

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Sheet 3 of 3

FORM PTO-1449.	ATTY. DOCKET NO. 200301885-2	APPLICATION NO.	CONFIRMATION NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	APPLICANT Samuel H. Duncan		
(Use several sheets if necessary)	FILING DATE July 1, 2003	GROUP N/A	

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3A				
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3L					
3M					
3N					
3O					
3P					

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

W ✓	3Q	New Compaq AlphaServer GS Series Architecture White Paper, Compaq Computer Corporation, (c) 2000, pp. 1-11.
	3R	
	3S	

EXAMINER

DATE CONSIDERED

5/12/04